SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Civil Engineering				
Date	**Name of the Lab**	**Timings**		
18-11-2026 (Wednesday)	Building Planning and Drawing Lab (CE4509)	**1.30 p.m. - 04.30 p.m.**		
		Section - A (25481A0101 - 154 & 26485A0101 - 118, **Supple:** 23481A0108)		
19-11-2026 (Thursday)	Strength of Materials Lab (CE4508)	**9.30 a.m. - 12.30 p.m.**	**11.30 a.m. - 02.30 p.m.**	**01.30 p.m. - 04.30 p.m.**
		Section - A (25481A0101 - 127)	Section - A (25481A0128 - 150)	Section - A (25481A0151 - 154 & 26485A0101 - 118 **Supple:** 23481A0108)
20-11-2026 (Thursday)	Design Thinking & Innovation (DT4501)	Section - A (25481A0101 - 127)	Section - A (25481A0128 - 150)	Section - A (25481A0151 - 154 & 26485A0101 - 118 **Supple:** 24481A0126)
23-11-2026 (Monday)	Surveying Lab (CE4507)	Section - A (25481A0101 - 127)	Section - A (25481A0128 - 150)	Section - A (25481A0151 - 154 & 26485A0101 - 118 **Supple:** 23481A0108)

Controller of Examinations

Principal

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Tables of Principal/Vice Principal (Academics)/Vice Principal (Administration)

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Electrical and Electronics Engineering				
Date	**Name of the Lab**	**Timings**		
		9.30 a.m. - 12.30 p.m.	**11.30 a.m. - 02.30 p.m.**	**01.30 p.m. - 4.30 p.m.**
18-11-2026 (Friday)	DC Machines and Transformers Lab (EE4511)	Section - B (25481A0241 - 260, 24481A0224 & 256)	Section - B (25481A0261 - 274 & 26485A0230 - 237)	Section - B (26485A0238 - 259)
23-11-2026 (Monday)		Section - A (25481A0201 - 223)	Section - A (25481A0224 - 239 & 26485A0201 - 207)	Section - A (26485A0208 - 229)
19-11-2026 (Thursday)	Data Structures Using C Lab (CT4506)	Section - A (25481A0201 - 223)	Section - A (25481A0224 - 239 & 26485A0201 - 207)	Section - A (26485A0208 - 229)
20-11-2026 (Friday)		Section - B (25481A0241 - 260, 24481A0224 & 256)	Section - B (25481A0261 - 274 & 26485A0230 - 237)	Section - B (26485A0238 - 259)
18-11-2026 (Friday)	Electrical Circuit Analysis-II and Simulation Lab (EE4510)	Section - A (25481A0201 - 223)	Section - A (25481A0224 - 239 & 26485A0201 - 207)	Section - A (26485A0208 - 229 **Supple:** 23481A0201 & 24481A0280,252)
23-11-2026 (Monday)		Section - B (25481A0241 - 260, 24481A0224 & 256)	Section - B (25481A0261 - 274 & 26485A0230 - 237)	Section - B (26485A0238 - 259)

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(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Mechanical Engineering			
Date	**Name of the Lab**	**Timings**	
		9.30 a.m. - 12.30 p.m.	**01.30 p.m. - 4.30 p.m.**
23-11-2026 (Monday)	Mechanics of Solids and Materials Science Lab (ME4506)	Section - A (25481A0301 - 333)	Section - A (25481A0334 - 349 & 26485A0301 - 317 & 24481A0311, 333)
18-11-2026 (Wednesday)	Computer- Aided Machine Drawing Lab (ME4507)	Section - A (25481A0301 - 333)	Section - A (25481A0334 - 349 & 26485A0301 - 317 & 24481A0311, 333)
19-11-2026 (Thursday)	Python Programming Lab (CT4507)	Section - A (25481A0301 - 333)	Section - A (25481A0334 - 349 & 26485A0301 - 317 & 24481A0311, 333)
20-11-2026 (Friday)	Embedded Systems and IoT Lab (EC4501)	Section - A (25481A0301 - 333)	Section - A (25481A0334 - 349 & 26485A0301 - 317 & 24481A0311, 333)
21-11-2026 (Saturday)	Design Thinking & Innovation (DT4501)	Section - A (25481A0301 - 349 & 26485A0301 - 317 & 24481A0311, 333)	.....

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Tables of Principal/Vice Principal (Academics)/Vice Principal (Administration)

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Electronics and Communication Engineering				
Date	**Name of the Lab**	**Timings**		
		9.30 a.m. - 12.30 p.m.	**11.30 a.m. - 02.30 p.m.**	**01.30 p.m. - 4.30 p.m.**
18-11-2026 (Wednesday)	Electronic Devices and Circuits Lab (EC4509)	Section - B (25481A0465 - 488)	Section - B (25481A0489 - 4B2)	Section - B (25481A04B3 - 4C8 & 26485A0410 - 417)
19-11-2026 (Thursday)		Section - C (25481A04C9 - 4F2)	Section - C (25481A04F3 - 4H6)	Section - C (25481A04H7 - 4J2 & 26485A0418 - 425)
20-11-2026 (Friday)		Section - D (25481A04J3 - 4L6)	Section - D (25481A04L7 - 4N9)	Section - D (25481A04O0 - 4P4 & 26485A0426 - 434 & 24481A0414)
23-11-2026 (Monday)		Section - A (25481A0401 - 424)	Section - A (25481A0425 - 448)	Section - A (25481A0449 - 464 & 26485A0401 - 409 **Supple:** 23481A04A5, 4A7, 4J6, 4N2)
18-11-2026 (Wednesday)	Signals and Systems Lab (EC4510)	Section - A (25481A0401 - 436)	….	Section - A (25481A0437 - 464 & 26485A0401 - 409 **Supple:** 24481A0469)
19-11-2026 (Thursday)		Section - B (25481A0465 - 4C8)	….	Section - B (25481A04A1 - 4C8 & 26485A0410 - 417)
20-11-2026 (Friday)		Section - C (25481A04C9 - 4G4)	….	Section - C (25481A04G5 - 4J2 & 26485A0418 - 425)
23-11-2026 (Monday)		Section - D (25481A04J3 - 4M8)	….	Section - D (25481A04M9 - 4P4, 26485A0426 - 434 & 24481A0414)

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026
TIME TABLE

Date: 11-11-2026

Branch - Electronics and Communication Engineering			
		9.30 a.m. - 12.30 p.m.	**01.30 p.m. - 4.30 p.m.**
18-11-2026 (Wednesday)	Data Structures using Python Lab (CT4508)	Section - D (25481A04J3 - 4P4 & 26485A0426 - 434, 24481A0414)	**….**
19-11-2026 (Thursday)		Section - A (25481A0401 - 464 & 26485A0401 - 409)	**Supple**: 23481A04J6, 4Q2, 25485A0418
20-11-2026 (Friday)		Section - B (25481A0465 - 4C8 & 26485A0410 - 417)	**….**
23-11-2026 (Monday)		Section - C (25481A04C9 - 4J2 & 26485A0418 - 425)	**….**

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Principal

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SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

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Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Computer Science and Engineering

Date	Name of the Lab	Timings	
		9.30 a.m. - 12.30 p.m.	**1.30 p.m. - 4.30 p.m.**
18-11-2026 (Friday)	Advanced Data Structures and Algorithms Lab (CT4513)	Section - C (25481A05D5 - 5K0 & 24481A05A1)	Section - D (25481A05K1 - 5Q6 & 24481A05M3)
19-11-2026 (Thursday)		Section - E (25481A05Q7 - 5X2 & 24481A05Y7)	Section - F (26485A0501 -546, **Supple:** 24481A05X0)
23-11-2026 (Monday)		Section - A (25481A0501 - 567)	Section - B (25481A0568 - 5D4)
18-11-2026 (Friday)	Object Oriented Programming Through Java Lab (CT4514)	Section - A (25481A0501 - 567)	Section - B (25481A0568 - 5D4, **Supple:** 24481A05X0)
19-11-2026 (Thursday)		Section - C (25481A05D5 - 5K0 & 24481A05A1)	Section - D (25481A05K1 - 5Q6 & 24481A05M3)
23-11-2026 (Monday)		Section - E (25481A05Q7 - 5X2 & 24481A05Y7)	Section - F (26485A0501 -546)
18-11-2026 (Friday)	Python Programming Lab (CT4515)	Section - E (25481A05Q7 - 5X2 & 24481A05Y7)	Section - F (26485A0501 -546)
19-11-2026 (Thursday)		Section - A (25481A0501 - 567)	Section - B (25481A0568 - 5D4)
23-11-2026 (Monday)		Section - C (25481A05D5 - 5K0 & 24481A05A1)	Section - D (25481A05K1 - 5Q6 & 24481A05M3 **Supple:** 24481A05X0)

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SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Information Technology

Date	Name of the Lab	Timings	
		9.30 a.m. - 12.30 p.m.	1.30 p.m. - 4.30 p.m.
18-11-2026 (Wednesday)	Advanced Data Structures and Algorithms Lab (CT4513)	Section - C (25481A012C1- 12I0 & 26485A1215 - 1219, 23481A12H7)	Section - D (25481A012I1- 12O0 & 26485A1220 - 1225, 24481A12I7)
23-11-2026 (Monday)		Section - A (25481A01201 - 1260 & 26485A1201 - 1206, 24481A1236)	Section - B (25481A01261 - 12C0 & 26485A1207 - 1214, 23481A12A9)
18-11-2026 (Wednesday)	Object Oriented Programming Through Java Lab (CT4514)	Section - A (25481A01201 - 1260 & 26485A1201 - 1206, 24481A1236)	Section - B (25481A01261 - 12C0 & 26485A1207 - 1214, 23481A12A9)
19-11-2026 (Thursday)		Section - C (25481A012C1- 12I0 & 26485A1215 - 1219, 23481A12H7)	Section - D (25481A012I1- 12O0 & 26485A1220 - 1225, 24481A12I7)
19-11-2026 (Thursday)	Python programming Lab (CT4515)	Section - A (25481A01201 - 1260 & 26485A1201 - 1206, 24481A1236)	Section - B (25481A01261 - 12C0 & 26485A1207 - 1214, 23481A12A9)
23-11-2026 (Monday)		Section - C (25481A012C1- 12I0 & 26485A1215 - 1219, 23481A12H7)	Section - D (25481A012I1- 12O0 & 26485A1220 - 1225, 24481A12I7)
20-11-2026 (Friday)	Design Thinking & Innovation (DT4501)	Section - A (25481A01201 - 1265 & 26485A1201 - 1207, 24481A1236)	Section - B (25481A01266 - 12D0 & 26485A1208 - 1214, 23481A12A9)
21-11-2026 (Saturday)		Section - C (25481A012D1- 12J5 & 26485A1215 - 1221, 23481A12H7)	Section - D (25481A012J6- 12Q0 & 26485A1222 - 1228, 24481A12I7)

Controller of Examinations

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Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026
TIME TABLE

Date: 11-11-2026

Branch - CSE (Artificial Intelligence & Machine Learning)			
Date	**Name of the Lab**	**Timings**	
		9.30 a.m. - 12.30 p.m.	**1.30 p.m. - 4.30 p.m.**
18-11-2026 (Wednesday)	Object Oriented Programming Through Java Lab (CT4514)	Section - A (25481A4201 - 4265 & 26485A4201 - 4208)	Section - B (25481A4266 - 42D0 & 26485A4209 - 4214, 24481A42A8, 42B9)
19-11-2026 (Thursday)	Python Programming Lab (CT4515)	Section - A (25481A4201 - 4265 & 26485A4201 - 4208)	Section - B (25481A4266 - 42D0 & 26485A4209 - 4214, 24481A42A8, 42B9)
20-11-2026 (Friday)	Design Thinking & Innovation (DT4501)	Section - A (25481A4201 - 4265 & 26485A4201 - 4208)	Section - B (25481A4266 - 42D0 & 26485A4209 - 4214, 24481A42A8, 42B9)
23-11-2026 (Monday)	Advanced Data Structures and Algorithms Lab (CT4513)	Section - A (25481A4201 - 4265 & 26485A4201 - 4208)	Section - B (25481A4266 - 42D0 & 26485A4209 - 4214, 24481A42A8, 42B9 **Supple:** 23481A42B0)

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SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)
Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026
TIME TABLE

Date: 11-11-2026

Branch - Artificial Intelligence & Data Science

Date	Name of the Lab	Timings	
		9.30 a.m. - 12.30 p.m.	**1.30 p.m. - 4.30 p.m.**
19-11-2026 (Thursday)	Advanced Data Structures Algorithms & Database Lab (AD4501)	Section - A (25481A5401 - 5462 & 26485A5401 - 5410)	Section - B (25481A5463 - 54C4 & 26485A5411 - 5420)
23-11-2026 (Monday)		Section - C (25481A54C5 - 54I5 & 26485A5421 - 5428, 24481A5403, 5463)	....
18-11-2026 (Wednesday)	Object Oriented Programming Through Java Lab (CT4514)	Section - C (25481A54C5 - 54I5 & 26485A5421 - 5428, 24481A5403, 5463)	....
23-11-2026 (Monday)		Section - A (25481A5401 - 5462 & 26485A5401 - 5410)	Section - B (25481A5463 - 54C4& 26485A5411 - 5420)
18-11-2026 (Wednesday)	Python Programming Lab (CT4515)	Section - A (25481A5401 - 5462 & 26485A5401 - 5410)	Section - B (25481A5463 - 54C4& 26485A5411 - 5420)
19-11-2026 (Thursday)		Section - C (25481A54C5 - 54I5 & 26485A5421 - 5428, 24481A5403, 5463)	....

Controller of Examinations

Principal

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Tables of Principal/Vice Principal (Academics)/Vice Principal (Administration)

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

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Seshadri Rao Knowledge Village, Gudlavalleru - 521 356, Krishna District, A.P.

Examination Section

II B.Tech I Semester (R23) Regular/Supplementary Practical Examinations, November 2026

TIME TABLE

Date: 11-11-2026

Branch - Internet of Things

Date	Name of the Lab	Timings		
		9.30 a.m. - 12.30 p.m.	**11.30 a.m. - 02.30 p.m.**	**01.30 p.m. - 4.30 p.m.**
18-11-2026 (Wednesday)	Data Structures using Python Lab (CT4508)	Section - B (25481A6054 - 6085)	....	Section - B (25481A6086 - 60A5 & 26485A6009 - 6016, 24481A6075 **Supple:** 24481A6031)
23-11-2026 (Monday)		Section - A (25481A6001 - 6032)	....	Section - A (25481A6033 - 6053 & 26485A6001 - 6008)
18-11-2026 (Wednesday)	Design Thinking & Innovation (DT4501)	**9.30 a.m. - 04.30 p.m.**		
		Section - A (25481A6001 - 6053 & 26485A6001 - 6008)		
23-11-2026 (Monday)		Section - B (25481A6054 - 60A5 & 26485A6009 - 6016, 24481A6075)		
19-11-2026 (Thursday)	Analog IC Applications Lab (EC4504)	Section - B (25481A6054 - 6075)	Section - B (25481A6076 - 6096)	Section - B (25481A6097 - 60A5 & 26485A6009 - 6016, 24481A6075)
20-11-2026 (Friday)		Section - A (25481A6001 - 6021)	Section - A (25481A6022 - 6042)	Section - A (25481A6043 - 6053 & 26485A6001 - 6008)
19-11-2026 (Thursday)	Signal Processing and Communications Lab (EC4505)	Section - A (25481A6001 - 6021)	Section - A (25481A6022 - 6042)	Section - A (25481A6043 - 6053 & 26485A6001 - 6008 **Supple:** 24481A6031)
20-11-2026 (Friday)		Section - B (25481A6054 - 6075)	Section - B (25481A6076 - 6096)	Section - B (25481A6097 - 60A5 & 26485A6009 - 6016, 24481A6075)

Controller of Examinations

Principal

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